

**AMENDMENTS TO THE CLAIMS**

1-41. (Cancelled)

42. (Previously Presented) A method for manufacturing a light emitting device, the method comprising the steps of:

forming a buffer layer;

forming an N-type gallium nitride layer on the buffer layer;

forming a super lattice structure including InGa<sub>N</sub> on the N-type gallium nitride layer, wherein the super lattice structure is not doped with an n-type impurity,

wherein the super lattice structure including InGa<sub>N</sub> includes a plurality of first InGa<sub>N</sub> layers and a plurality of second InGa<sub>N</sub> layers,

wherein each of the plurality of first InGa<sub>N</sub> layers has an In composition less than an In composition of each of the plurality of second InGa<sub>N</sub> layers, and

wherein the first InGa<sub>N</sub> layer is directly on the N-type gallium nitride layer;

forming an active layer on the super lattice structure and including an InGa<sub>N</sub>/InGa<sub>N</sub> structure of a multi-quantum well structure,

wherein the active layer is directly on one of the plurality of second InGa<sub>N</sub> layers; and

forming a P-type gallium nitride layer on the active layer,

wherein the super lattice structure including InGa<sub>N</sub> has a plurality of pits formed thereon and wherein a non-zero number of the plurality of pits is 50 or less per area of 5μm×5μm, and

wherein the buffer layer is grown at a first temperature,

wherein the first InGaN layer of the super lattice structure including InGaN is grown at a second temperature higher than the first temperature,

wherein the second InGaN layer of the super lattice structure including InGaN is grown at a third temperature higher than the first temperature and lower than the second temperature, and

wherein the active layer is grown at a fourth temperature of 600~800°C and the fourth temperature is lower than the second temperature and the third temperature.

43-46. (Cancelled)

47. (Previously Presented) The method according to claim 42, wherein the super lattice structure including InGaN includes an  $\text{In}_x\text{Ga}_{1-x}\text{N}/\text{In}_y\text{Ga}_{1-y}\text{N}$  multi-layer.

48. (Previously Presented) The method according to claim 42, wherein each layer of the super lattice structure including InGaN has a thickness of 1~3000 Å.

49. (Previously Presented) The method according to claim 42, wherein the super lattice structure including InGaN has a photoluminescence characteristic of a yellow band intensity/N-doped GaN intensity ratio of 0.4 or below.

50. (Cancelled)

51. (Previously Presented) A light emitting diode (LED), comprising:

- a substrate;
- a buffer layer on the substrate;
- an undoped GaN layer on the buffer layer;
- a GaN layer between the buffer layer and the undoped GaN layer;
- an N-type GaN layer directly on the undoped GaN layer;
- a super lattice structure including InGaN directly on the N-type GaN layer,

wherein the super lattice structure is not doped with an n-type impurity,

wherein the super lattice structure including InGaN includes a plurality of first layers and a plurality of second layers,

wherein each of the plurality of first layers has an In composition less than an In composition of each of the plurality of second layers,

wherein the first layer is directly on the N-type GaN layer,

wherein each of the first layers has a thickness of 1~3000 Å, and

wherein each of the second layers has a thickness of 1~3000 Å;

an active layer on the super lattice structure including an InGaN/InGaN structure of a multi-quantum well structure, wherein the active layer is directly on the second layer; and

a P-type GaN layer on the active layer,

wherein the super lattice structure including InGaN has a plurality of pits thereon and wherein a non-zero number of the plurality of pits is 50 or less per area of  $5\mu\text{m}\times 5\mu\text{m}$ .

52. (Cancelled)

53. (Previously Presented) The LED according to claim 51, wherein the undoped GaN layer is directly on the GaN layer.

54. (Cancelled)

55. (Previously Presented) The method according to claim 42, further comprising:  
forming an undoped GaN layer on the buffer layer before forming the N-type gallium nitride layer.

56. (Previously Presented) The method according to claim 55, wherein the undoped GaN layer is grown at a fifth temperature higher than the first temperature, the second temperature, the third temperature and the fourth temperature.

57. (Previously Presented) The method according to claim 42, further comprising:  
forming a plurality of pits between the active layer and the P-type gallium nitride layer.

58. (Previously Presented) The method according to claim 42, wherein the step of forming a super lattice structure comprises:  
forming the super lattice structure using an alkyl source including TMGa and TMIIn and a hydride gas including  $\text{NH}_3$  and  $\text{N}_2$ .

59. (Previously Presented) The LED according to claim 51, wherein the super lattice structure is formed using an alkyl source including TMGa and TMIIn and a hydride gas including NH<sub>3</sub> and N<sub>2</sub>.